Design of Metamorphic Dual-Junction InGaP/GaAs Solar Cell on Si with Efficiency Greater than 29% using Finite Element Analysis

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Abstract — Heterogeneous integration of multijunction III-V solar cells on Si is a promising solution for the widespread commercialization of III-V cells. However, the polar on non-polar epitaxy and 4% lattice-mismatch between GaAs and Si results in formation of defects and dislocations, which can significantly impede the minority carrier lifetime and hence the cell performance. We have investigated the impact of threading dislocation density on the performance of dual-junction (2J) n+/p InGaP/GaAs solar cells on Si. Using our calibrated model, the metamorphic 2J cell on Si was optimized by tailoring the 2J cell design on Si to achieve current-matching between the subcells at a realistic threading dislocation density of 10⁶ cm⁻². We present a novel 2J InGaP/GaAs cell design on Si at a threading dislocation density of 10^6 cm⁻² which exhibited a theoretical conversion efficiency of greater than 29% at AM1.5G spectrum, indicating a path for viable III-V multijunction cell technology on Si.

Index Terms — III-V semiconductor materials, charge carrier lifetime, epitaxial layers, photovoltaic cells, semiconductor device modeling, short circuit currents, silicon.

I. INTRODUCTION

III-V multijunction solar cells have been the primarily candidate for space satellites. However, the use of III-V cells for terrestrial applications is currently limited by the size and cost of Ge and III-V substrates. Heterogeneous integration of such III-V cells on large diameter and cheaper Si substrate can not only substantially lower the cost of III-V cells, but also increase their yield per die. Furthermore, implementation of III-V solar cells on Si in conjunction with substrate re-use technologies [1], [2] can lead to additional cost reduction. However, viability of III-V InGaP/GaAs solar cells on Si relies on the ability to grow high quality GaAs on Si with careful lattice engineering and substrate treatment. The polar on non-polar epitaxy and the 4% lattice-mismatch between GaAs and Si may results in formation of various defects including dislocations. These dislocations can propagate into the photoactive cell region, significantly impeding the minority carrier lifetime and hence the overall cell performance. Therefore, it becomes extremely important to correlate the impact of dislocation density on the performance of metamorphic III-V tandem solar cells on Si.

The focus of this paper is to provide a systematic study on the correlation of threading dislocation density (TDD) and minority carrier lifetime on the photovoltaic performance of 2J InGaP/GaAs solar cell on Si. The schematic of the structure investigated in this paper is shown in Fig. 1. As a starting point, the base thicknesses in the GaAs and InGaP cell were set to be 2.5 μ m and 0.9 μ m, respectively [3]. We then optimized the 2J n+/p InGaP/GaAs cell design on Si by achieving current-matching condition between the subcells at a TDD of 10⁶ cm⁻². The results from our work provide a quantitative assessment of the solar cell figure-of-merits as a function of TDD, thus enabling more efficient cell design and prediction of the metamorphic 2J InGaP/GaAs cell performance on Si.

The impact of TDD on cell performance has been previously investigated [4]-[9], however, their analysis was limited to 1J GaAs cell on Si. J_{sc} used for modeling the impact of TDD on V_{oc} was assumed to be independent of TDD [8]. However, in reality J_{sc} decreases with increase in TDD and may have a significant impact on the cell efficiency. There has not been significant work done on the modeling-assisted design of metamorphic tandem cells incorporating the impact of TDD. This paper provides the first study on modeling & optimization of metamorphic 2J n+/p InGaP/GaAs cell design on Si at AM1.5G spectrum using finite element analysis without assuming a constant J_{sc} .

II. SIMULATION MODEL

Using the model described below, coupled with the material parameters summarized in Table I, we computed the impact of

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	Metal Con	tact		E ZnS
	n⁺⁺-GaAs	Сар		ARC-MgF2-
n-In _{0.5} Al _{0.5} P	Window	1.95x10 ¹⁸	20 nm	n cell
n-In _{0.49} Ga _{0.51} P	Emitter	2x10 ¹⁸	0.1 µm	InGaP eV) (1.86 eV)
p-In _{0.49} Ga _{0.51} P	Base	1.5x10 ¹⁷	0.3-1.1 µm	ì
p-In _{0.5} Ga _{0.25} Al _{0.25} P	BSR	2x10 ¹⁸	50 nm	iion
p-Al _{0.3} Ga _{0.7} As	TJ	5x10 ¹⁹	10 nm	Tunnel junction
n-Al _{0.01} Ga _{0.99} As	TJ	2x10 ¹⁹	10 nm	
n-In _{0.49} Ga _{0.51} P	Window	2x10 ¹⁸	50 nm	10
n-GaAs	Emitter	2x10 ¹⁸	0.1 µm	GaAs cent
p-GaAs	Base	1x10 ¹⁷	1-3 µm	(1.7
p-In _{0.49} Ga _{0.51} P	BSR	2x10 ¹⁸	70 nm	Her
p-GaAs nucle	ation and bu	iffer layer	2.5 µm	Bulle
p-typ	e Si substra	te ~ 2x10 ¹⁸		Substrate
				Metal CO.

Fig. 1. Schematic depiction of 2J InGaP/GaAs solar cell on Si.

TABLE I GaAs AND InGaP MATERIAL AND TRANSPORT PARAMETERS

Parameter		Value
	<u>GaAs Subcell</u>	<u>InGaP Subcell</u>
Band-gap (E _g) Minority electron mobility ($\mu_{e^{\cdot}}$) Minority hole mobility ($\mu_{h^{\cdot}}$) Electron diffusion coefficient (D _n) at 300 K Hole diffusion coefficient (D _p) at 300 K Peak minority electron life-time (τ°_{n}) Effective minority electron life-time (τ_{n}) Minority hole life-time (τ°_{p})	$\begin{array}{c} 1.424 \ eV\\ 3088.8 \ cm^2/Vs\\ 100 \ cm^2/Vs \ [11]\\ 80 \ cm^2/s \ [12]\\ 2.59 \ cm^2/s\\ 20 \ ns \ [7], \ [13]\\ f \ (\tau_{TDD})\\ 2.5 \ ns \end{array}$	1.86 eV 1074 cm ² /Vs [14] 40 cm ² /Vs [14] 27.816 cm ² /s 1.036 cm ² /s 10 ns [15] f (τ_{TDD}) 1 ns

of TDD on the minority carrier base lifetime. This variation in this lifetime with increasing TDD, along with the material parameters incorporated from Table I, was used to model the impact of TDD on the photovoltaic performance of the 2J InGaP/GaAs cells on Si using finite element analysis [10].

Minority carrier lifetime is one of the most important figureof-merit for metamorphic cell design. Defects and dislocations which are generated at the III-V/Si hetero-interface may serve as recombination centers and impede the minority carrier lifetime and hence their diffusion length. This degradation in lifetime may significantly impede the cell performance [4]-[6]. The effective minority carrier lifetime (τ_n or τ_p) in a latticemismatched system varies as a function of TDD (f (τ_{TDD})) [7], [16] and can be expressed as,

$$\frac{1}{\tau_{n,p}} = \frac{1}{\tau_{n,p}^{0}} + \frac{1}{\tau_{TDD}}$$
(1)

where τ°_{p} and τ°_{n} are the minority carrier lifetime for a dislocation free material. The τ_{TDD} is the minority carrier lifetime associated with the recombination at dislocation which can be expressed as,

$$\tau_{TDD} = \frac{4}{\pi^3 (TDD)D} \tag{2}$$

where D is the minority carrier diffusion coefficient and TDD is the threading dislocation density in cm^{-2} .

The impact of TDD on minority carrier base lifetime in GaAs and InGaP cell is shown in Fig. 2. The effective lifetimes in p-GaAs ($N_A=1x10^{17}$ cm⁻³) and p-InGaP ($N_A=1.5x10^{17}$ cm⁻³) were calculated using (1) and (2) with the parameters from Table I. The experimental lifetime values for p-GaAs (red) [13] and p-InGaP (green) [19] are included in this figure, indicating an excellent agreement between the model and experiment. It can be noted that for TDD higher than ~10⁴ cm⁻² in GaAs, the lifetime significantly degraded as minority electrons get recombined due to the high TDD. The onset of degradation in minority carrier lifetime began at a

higher TDD ($\sim 10^5$ cm⁻²) in InGaP cell compared to GaAs cell due to the lower electron diffusion coefficient in InGaP.

In a solar cell, the minority carriers that are generated in the thick base, far from the depletion region, need to have high lifetime to reach the junction before being recombined. Hence, the variation of minority electron lifetime in the p-GaAs and p-InGaP base had significant impact on the cell performance. On the other hand, minority holes generated in the thin n-type emitter had to travel much shorter lengths to reach the junction. Hence, minority hole lifetime was considered to be constant. The surface recombination velocity (SRV) was set to 10^4 cm/s for both holes (S_p) and electrons (S_n) at InGaP base/back reflector interface and emitter/window interface. The corresponding S_n and S_p values were set to 10^6 cm/s at both the interfaces in the GaAs subcell. In our model, the mobility of minority carriers was assumed to be independent of TDD [16]. Also, the band-gap narrowing effect and the shadowing effect were not included in our model. Hence, the analysis discussed in this paper provides an upper bound for the modeled cell results.



Fig. 2. Correlation of threading dislocation density on minority electron lifetime in p-GaAs and p-InGaP base.

We calibrated our model with the 2J InGaP/GaAs cell structure in [17], [18]. A τ_n of 5.2 ns was used as reported [17]. We determined that this lifetime corresponds to a TDD of ~4x10⁵ cm⁻² as shown in Fig. 2. Since, the value of τ_n was not provided for the p-GaAs, a τ_n of 3.3 ns was considered as calculated in Fig. 2. The simulation results are compared to the experimental results in Table II. Overall, simulated and the experimental values presented in Table II are in agreement, thus validating our model and the parameters utilized in this simulation study.

TABLE II	
MODEL CALIBRATION WITH 2J InGaP/GaAs EXPERIMENTAL DA	ΤA

	V _{oc}	J _{sc}	FF	Efficiency
	(V)	(mA/cm ²)	(%)	(%)
Experiment [17]	2.48	14.22	85.6	30.28
Simulation	2.41	13.85	88.9	29.80
Experiment [18]	2.52	12.70	85.00	27.20
Simulation	2.58	12.53	85.19	27.64

III. RESULTS AND DISCUSSIONS

This section is divided into two subsections. In the subsection A, the impact of threading dislocation density on the performance of 2J n+/p InGaP/GaAs cell on Si is discussed. Subsection B describes our approach of realizing the current-matching condition between each subcell by tailoring the 2J InGaP/GaAs cell design on Si at a realistic TDD of 10^6 cm⁻².

A. 2J InGaP/GaAs Cell on Si

For the analysis of metamorphic 2J InGaP/GaAs cell on Si, the base thicknesses in the GaAs and InGaP subcells were initially set to 2.5 μ m and 0.9 μ m, respectively. The TDD was varied from 10⁵ to 10⁸ cm⁻² and it was assumed that all the threading dislocations in GaAs bottom subcell propagated to the top InGaP subcell.

Voltage at maximum power point, V_m and V_{oc} were plotted as a function of increasing TDD in the 2J InGaP/GaAs cell on Si as shown in Fig. 3(a). Both V_{oc} and V_m degraded with increasing TDD. The primary reason for the decrease in V_{oc} at higher TDD was due to its strong dependence on the reverse saturation current density, J_{02} , associated with depletion region recombination. The V_{oc} can be expressed as,

$$V_{oc} = \left(\frac{n_2 kT}{q}\right) \ln\left(\frac{J_{sc}}{J_{02}}\right)$$
(3)

where J_{02} depends on the minority lifetime in the base, τ_{base} and can be expressed as,

$$J_{02} = \frac{q n_i W_D}{2} \left(\frac{1}{\tau_{base}} \right) \tag{4}$$

where n_i is the intrinsic carrier concentration and W_D is the depletion layer width. At higher TDD, the value of J_{02} increased due to the reduction in the minority electron lifetime in the base, τ_{base} . Thus, the increase in J_{02} led to significant degradation of both V_{oc} and V_m at higher TDD.

Current density at maximum power point, J_m and J_{sc} were plotted as a function of increase in TDD in Fig. 3(b). Beyond a TDD of 10^6 cm⁻², the simultaneous reduction in minority electron lifetime in p-GaAs and p-InGaP led to the degradation



Fig. 3. Impact of threading dislocation density variation on 2J InGaP/GaAs cell performance parameters: (a) V_{oc} and V_{m} , (b) J_{sc} and J_m and (c) η at AM1.5G.

in J_{sc} . From a TDD of 10^5 to 10^8 cm⁻², the degradation in J_m was found to be greater than the degradation in J_{sc} . Furthermore; the onset of degradation in J_{sc} began at higher TDD compared to V_{oc} in the 2J cell. Thus, J_{sc} was found to be more tolerant to TDD compared to V_{oc} , consistent with prior work [9].

Even at a TDD of 10^7 cm^{-2} ($\tau_n=0.44 \text{ ns}$), the electron diffusion length in p-InGaP base was sufficient to travel the 0.9 µm thick base. However, we observed that the onset of degradation in the J_{sc} began at a lower TDD of $\sim 10^6 \text{ cm}^{-2}$ instead at a TDD of 10^7 cm^{-2} (Fig. 2). This was attributed to the earlier onset of degradation in the minority electron lifetime in p-GaAs compared to the p-InGaP. Hence, the bottom GaAs subcell limited the overall J_{sc} in the 2J cell configuration.

Fig. 3(c) shows the degradation of efficiency in 2J InGaP/GaAs cell on Si as a function of increasing TDD. At a TDD of 10^6 cm⁻² in the 2J structure, the corresponding 2J cell efficiency was 26.22% with a minority electron lifetime of 1.49 ns and 3.17 ns in the GaAs and InGaP base, respectively. Above a TDD of 10^7 cm⁻², the 2J InGaP/GaAs cell efficiency degraded to that of 1J GaAs cell efficiency at the same TDD, thus making the contribution of the top InGaP cell redundant. Therefore, it becomes extremely important to optimize the metamorphic 2J cell structure on Si by carefully engineering the device structure, taking into account the detrimental impact the TDD may have on the cell performance.

B. Current-matching in 2J InGaP/GaAs Cell on Si

In a multijunction cell, one of the most important design criteria is to achieve the current-matching between the subcells. Current-matching enables to extract the best performance from a multijunction cell. The subcell providing the least photocurrent limits the overall current in a multijunction configuration. In our design, the subcells were not current-matched with the bottom GaAs subcell limiting the overall J_{sc} in the 2J configuration. Therefore, our tandem cell required appropriate design changes to achieve the currentmatching condition between the subcells. We varied the base thicknesses of both the GaAs and the InGaP subcells at an experimentally realistic TDD of 10^6 cm⁻² [20] to achieve the current-matching condition. At this TDD, the minority electron lifetime was calculated to be 1.494 ns and 3.171 ns in the p-GaAs and the p-InGaP base, respectively.

We first varied the thickness of p-InGaP base from 0.3 μ m to 1.1 μ m in a 1J InGaP cell configuration as shown by the blue curve in Fig. 4(a). Then, in the 2J cell configuration, the InGaP base thickness was again varied over the same range with the GaAs base thickness set to 1 μ m (pink curve) as shown in Fig. 4(a). The same procedure was repeated for the GaAs base thickness of 2 μ m (black line) and 3 μ m (red line). It can be seen that thinning the base thickness in 1J InGaP cell lowered the extracted J_{sc} as the absorption depth was lowered. Interestingly, thinning the top InGaP cell in 2J configuration allowed more photons to reach the bottom GaAs cell, increasing the J_{sc} contribution from the bottom subcell. This led to an overall increase of J_{sc} in the 2J configuration.

Utilizing the method discussed above, the current-matching condition was realized at points A and point B as shown in Fig. 4(a). The J-V characteristics of the 2J cell and the individual subcells corresponding to the point B were plotted in Fig. 4(b). At point B, the 2J cell exhibited a conversion efficiency of 29.62% with a 2 μ m and a 0.38 μ m thick GaAs and InGaP base, respectively. This efficiency was significantly higher than the 2J cell efficiency prior to the current-matching at the same TDD of 10⁶ cm⁻². The cell parameters extracted after achieving the current-matching condition at a TDD of 10⁶ cm⁻² were summarized in Table III. These results illustrate that an efficiency of greater than 29% can be realized for a metamorphic 2J InGaP/GaAs solar cell on Si even at a TDD of 10⁶ cm⁻². To further verify the results from this simulation study, experimental work is underway.



Fig. 4. a) Short-circuit current density as a function of top InGaP base thickness to optimize top and bottom cell base thicknesses for currentmatching, (b) Current-matched J-V characteristic in 2J InGaP/GaAs cell at AM 1.5G in for current-matched point B in Fig. 4 (a).

 TABLE III

 2J InGaP/GaAs CELL PARAMETERS AT AM 1.5G SPECTRUM

2J cell (InGaP/GaAs base thickness in μm)	V _{oc} (V)	J _{sc} (mA/cm ²)	FF (%)	Efficiency (%)
Non-optimized (0.9/2.5)	2.35	12.41	89.73	26.22
Current-matched (0.38/2)	2.37	14.18	88.22	29.62

IV. CONCLUSION

We have investigated the impact of threading dislocation density on the photovoltaic performance of 2J n+/p InGaP/GaAs solar cell on Si at AM 1.5 spectrum. The analysis indicates that a 2J InGaP/GaAs cell on Si exhibited a theoretical conversion efficiency of 26.22% with a 2.5 μ m and a 0.9 μ m thick GaAs and InGaP base, respectively. The onset of degradation in V_{oc} began at a lower TDD than in J_{sc}, indicating that V_{oc} was more sensitive to the threading dislocations.

The design of the 2J InGaP/GaAs cell on Si was optimized taking into account a realistic TDD of 10^6 cm⁻². By thinning the top InGaP cell from 0.9 µm to ~0.4 µm, the current-matching condition between the subcells was realized. As a result of current matching, the efficiency of the optimized structure increased to 29.62%. Thus, even in a lattice-mismatched 2J InGaP/GaAs cell on Si with TDD of 10^6 cm⁻², a theoretical conversion efficiency of greater than 29% at AM1.5G spectrum was achieved by tailoring the cell design. Once experimentally realized, this III-V cell technology on Si would offer a new paradigm for the advancement of low cost III-V solar cells and foster innovative avenues for both space and terrestrial applications.

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